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7590

11/02/2005

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01/18/2006 KBETEMAZ 00000072 122252 10692091

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Mark Salvatore

(Depositor's name)

*Mark Salvatore*

(Signature)

January 12, 2006

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/692,091	10/23/2003	Eric Hung	5201-27500	7976

TITLE OF INVENTION: MEMORY MODULE HAVING MIRRORED PLACEMENT OF DRAM INTEGRATED CIRCUITS UPON A FOUR-LAYER PRINTED CIRCUIT BOARD

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	02/02/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, DANG T	2824	365-063000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Daffer McDaniel LLP

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

LSI Logic Corporation

Milpitas, California, USA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

☒ Issue Fee☐ Publication Fee (No small entity discount permitted)☐ Advance Order - # of Copies \_\_\_\_\_

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11 JAN 06

Date

Typed or printed name Timothy R. CrollRegistration No. 36,771

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